TECH SUPPORT

MARINE RADAR EQUIPMENT RADAR 2000

FIELD SERVICE MANUAL

JRC Japan Radio Co., Ltd.

CONTENTS

SECTION 1

SPECIFICATIONS

1.1 GENERAL

1. Maximum range: 24 Nautical Miles
2. Minimum range: Less than 30 m on the 0.125 NM range
3. Range scales:

Range (NM)	Number of Rings	Range ring Interval (NM)
0. 125	2	0.0625
0.25	2	0.125
0.5	2	0.25
0.75	3	0.25
1.5	6	0.25
3.0	6	0.5
6.0	6	1.0
12.0	6	2.0
24.0	6	4.0

4. Range discriminations: Less than 35 m
5. Range ring accuracy: Better than ±0.9% of maximum range of the scale in use, or 8 m, whichever is the greater.
6. Bearing accuracy: ± 1 degree
7. Display device: CRT: 7" (640 x480 dots)
8. Environmental conditions:

Scanner unit	Temperature	- 15° C to +55° C
	Humidity	UP to 95% at 35° C
Display unit	Temperature	- 10° C to + 50° C
	Humidity	UP to 95% at 35° C

9. Input power: 10.2 V to 42V
10. Power consumption: 40 W
11. AVR: Floating AVR system

1.2 SCANNER UNIT

1. Dimensions:	Diameter	450mm
	Height	227mm
2. Weight:	Approx.	5.5 Kg
3. Polarization:	Horizontal	
4. Beamwidth:	Horizontal	6° nominal
	Vertical	25°
5. Sidelobes:	-21 dB or greater	
6. Rotation:	Approx. 24 rpm	
7. Transmitter frequency:	9445±30 MHz	
8. Peak power output:	2KW	
9. Pulse length/PRF:	0.08us/2250Hz (0. 125,0.25,0.5,0.75 NM)	
	0.35us/1500Hz (1.5, 3. 6 NM)	

	0.7us/750Hz (3, 6, 12, 24 NM)
10. Duplexer:	T-junction with diode Limiter
11. Mixer:	MIC frontend
12. IF amplifier:	Center frequency 60 MHz
	Bandwidth 3/10 MHz
13. Noise figure	Less than 6 dB
14. Characteristic	Semi-Log

1.3 DISPLAY UNIT

1. Dimensions:	Width	276 mm
	Depth	250 mm
	Height	204 mm
2. Weight:	Approx.	6 Kg
3. Mounting:	Table, Overhead, or Flush mount	
4. Video:	8 levels quantitized	
5. Tuning:	Auto/ Manual	
6. Bearing scale:	360° scale graduated at intervals of 1°	
7. VRM:	3 digit readout	
8. EBL:	3 digit readout	
9. Alarm:	Audible alarm with zone mark	
10. Off Center:	2/3 radius	
11. Planned TX:	Rotation period 10, 20 or 30 scans	
	Repetition period 3, 5, 10, 15 min.	
12. Language:	English, Spanish, Norwegian	
13. Features:	VRM(2), EBL(2), Cursor with LL, Interference rejection, Target expansion, Target alarm, LL or TD readout, Waypoint with LL, Offset, Timed TX, Target Trail, Auto tune, Man Overboard, floating EBL/VRM	
14. External input:		
NAV-AID	NMEA0183 (RMA, RMB, RMC, GLL, GTD, VTG, BWC sentence), NMEA0182	
Compass	NMEA0183 (HDM, HDT, VWH, or HSC sentence)	

SECTION 2

TECHNICAL DESCRIPTION

2.1 GENERAL

The theory of operation for the RADAR 2000 is presented here with descriptions following the functional block diagram circuits(Fig. 2-2).

2.2 SCANNER UNIT

The scanner unit consists of the RF PCB radiator, the motor, radiator rotating mechanism, bearing reset assembly, and the transmitter/receiver unit. These components are all housed within the 17.7" radome. The functional Block Diagram appears in Figure 2-1.

2.2.1 RADIATOR

The RF PCB radiator forms the main RF transmitting beam for the radar transmitter and becomes the receiving antenna during the receive cycle. The beam formed by the phased array styled PCB at half power points is 6° horizontally and 25° vertically. The direction of the beam (maximum radiated power) is essentially perpendicular to the radiator surface.

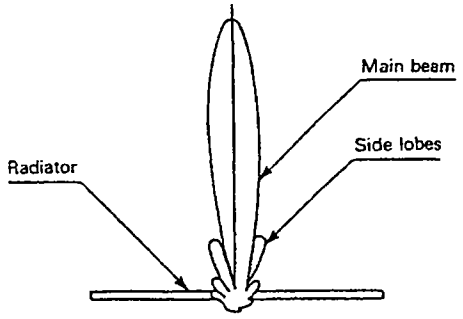


FIG. 2-1 RADIATION PATTERN

2.2.2 RADIATOR ROTATING MECHANISM

The mechanical coupling between the PCB radiator assembly and the motor encoder is effected by a reduction drive gearbox. The antenna motor normally rotates the radiator at approximately 24 rpm.

2.2.3 MOTOR-ENCODER

A 12VDC motor operating at a regulated 10VDC is used to rotate the radiator. At the bottom end of the motor an encoder section provides bearing pulses used for sweep line generation,

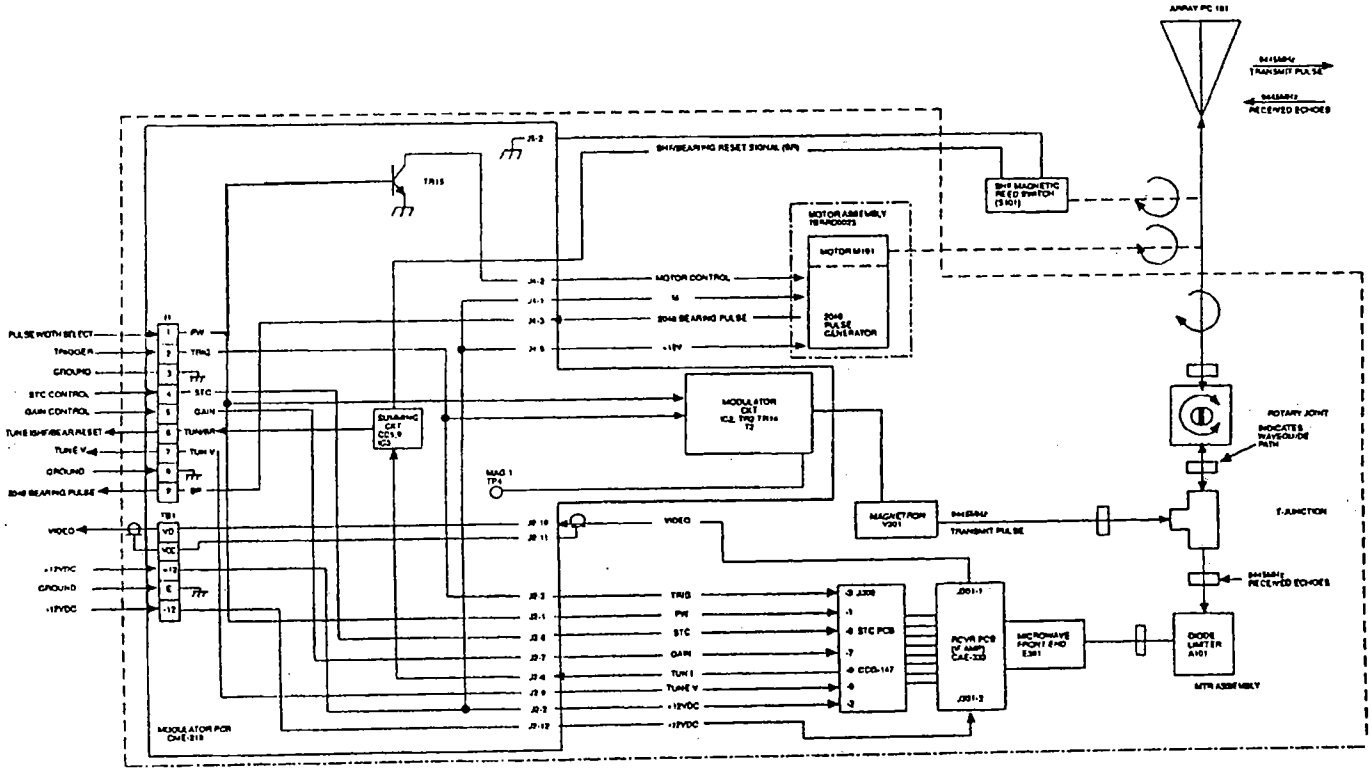


FIG. 2-2 BLOCK DIAGRAM OF SCANNER UNIT

transmitter trigger generation, and rotation synchronization of the sweep line within the display unit. A bearing sync pulse(BP) is generated for every 0.176 degrees of rotation(2048 pulses per each revolution) at 5VDC amplitude. These pulses are sent through J1-9 down to the Bearing Pulse circuitry in the display unit.

2.2.4 BEARING RESET SWITCH

The bearing reset switch, or otherwise referred to as the "heading reference switch", produces the signal to reset the scan converter circuitry to "0" when the permanent magnet fitted on the main gear passes across the reed switch S101. The reset signal (BZ) is sent down to the bearing reset circuitry in the display unit and synchronizes the scanner direction to the display sweep. The BZ signal is sent down to the display together with the Tune indication signal at J1-6.

2.3 TRANSMITTER

The transmitter consists of the solid state modulator circuits, the power supply, and the 2 kW magnetron.

2.3.1 Modulator(CME-274)

A solid state type pulsar design is used by the modulator and primarily consists of a pulse generator circuit, power MOSFET switch, and pulse transformer.

When setting the X-MIT/OFF key on the control panel at the display unit to "X-MIT", the transmitter trigger pulse is sent via the inter-unit cable from the transmit trigger generator circuit in the display unit to the J1-2 in the modulator.

Simultaneously +12 VDC and -12 CDC is supplied to the modulator to operate the HV power supply for the transmitter. The switching regulator power supply provides +250 VDC to charge the capacitor(C11 and C17). In addition to the high voltage for the modulator, the power supply also provides the magnetron filament voltage and the operating Vcc for the PFN control circuits TR9-TR12.

Generally the pulse width of the pulse generator circuit is controlled by the range key selectors on the front panel at the display unit. Three different pulse lengths: 0.08usec, 0.35usec and 0.7usec(in accordance with the range scale) can be provided. The pulse repetition frequency (PRF) always changes automatically to match the selected operating pulse length(See TABLE 1).

Upon receiving the positive trigger pulse, TR3 generate a differential waveform at C14, RV2, RV3 and RV4 setup the discharge period of this waveform to generate a short(80ns), medium(350ns), or long(700ns) gate to IC2-2. The pulse is amplified and applied to the gate of TR13 which will conduct for the selected pulse length drawing current through the primary of the Pulse Transformer(T2) generating a 1.8kV pulse to the magnetron cathode.

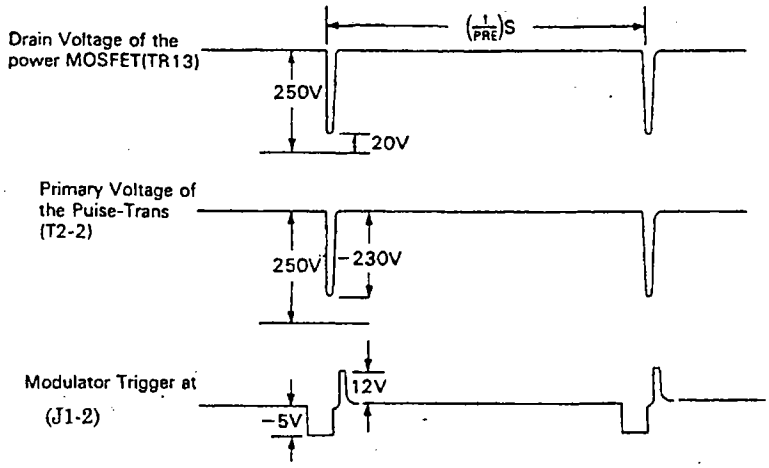


FIG. 2-3 TIME TABLE OF THE TRANSMITTER

TABLE 1 RANGE, PULSE LENGTH, AND PRF RELATIONSHIPS

Range	Pulse Length	PRF
0.125, 0.25, 0.5, 0.75 nm	0.08us	2250 Hz
1.5, 3, 6 nm	0.35us	1500 Hz
3, 6, 12, 24 nm	0.7us	750 Hz

2.4 RECEIVER UNIT

The receiver unit consists of the passive diode limiter(A101, NJS-6931J), the MIC Front End(E301, NJT-1946A) and the Receiver IF PCB(CAE-333) and GC CONTROL PCB(CCG-161).

The MIC Front End(E301, NJT-1946A) device consists of low-noise RF amplifier, a double balanced mixer, and the local oscillator. The received radar echo signals at 9445 MHz are first amplified by the low-noise RF amplifier. The signals are then sent into the double balanced mixer of the MIC. The MIC Local Oscillator, tuned by the adjustment of the operator's Tune control on the display unit frontpanel to be 60 MHz higher than the magnetron's operating frequency for maximum target detection, is also fed into the double balanced mixer. The balanced mixer output of 60 MHz echo signals is then coupled into the 60 MHz IF amplifier.

2.4.1 RECEIVER PCBs(CAE-333 and CCG-161)

The Receiver PCB includes the 60 MHz IF amplifier, bandwidth control circuits, video detector, tune circuitry, the GS CONTROL (CCG-161) and the video output circuitry.

IF Amplifier Circuit: The IF amplifier consists of low-noise gain controlled IC amplifiers IC1, IC2 and IC3, and bandwidth selector circuits TR1 and TR2.

IC1 and IC2 are controlled by the gain and GS control signals generated by the TR8, IC6, IC7. Maximum gain is obtained when the voltage level at IC1-5 and IC2-5 reaches 4 volts.

The bandwidth selector IC6(CCG-161) enables components to be activated in the amplifier circuit so the receiver has either a 10 MHz or a 3 MHz bandwidth characteristic. The selection depends on the pulse length selector signal (PW).

When no pulse length signal is present, IC6 will be "OFF" and the gate voltage of TR2 will be 0 volts. In this condition, the pulselength in operation is 0.08 μS and the bandwidth of the receiver is widened to 10 MHz. When the pulse length signal is other than 0.08 μS, IC6, will be turned "On", the gate of TR2 will be -4V, and the bandwidth will become narrow at 3 MHz.

2.4.2 Video Detector Circuit.(CAE-333)

The video detector circuits IC4, IC5, IC6 and IC7 operate as logarithmic amplifiers to remove the 60 MHz IF component from the incoming signals. The negative going signals appear across R17 where the IF component is removed by filter R14, L7. The detected signals, now at video frequency rates, are sent to the video output circuit.

2.4.3 Video Output circuit(CAE-333)

The video output circuit consists of emitter follower TR6. The emitter follower operates strictly as an impedance transformer to drive the 50 ohms coaxial cable which carries the video signal to the display unit. The video signal is shown in FIGURE 2-4.

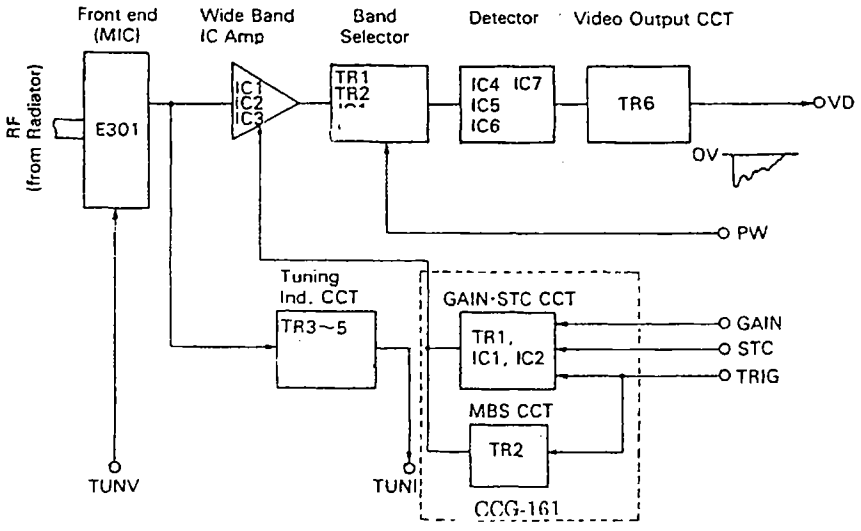


FIG. 2-4 RECEIVER UNIT BLOCK DIAGRAM

2.4.4 Tuning Indication Circuit

The tuning indicator circuit consists of amplifier TR3, detector TR4, and emitter follower TR5. TR charges C20(CCG-161)) to the detected signal voltage. This voltage is sent to the display unit as a tuning indication voltage via buffer amplifier IC1(CCG-161). The range of the tuning indication voltage varies normally between +4V(detuned) and -0.7V(tuned in long pulse).

2.4.5 STC Circuit(CCG-161)

The receiver includes GAIN, STC and Range rate circuitry composed of TR1, TR4, TR7, IC2 and IC4. The GAIN control voltage from the display unit is 12 volts for maximum sensitivity and 0 volts for minimum sensitivity. RV3 controls the GAIN DC threshold at the summing amplifier IC2. RV3 sets the maximum gain level for the receiver when +12 VDC is supplied at the GAIN input.

The STC control circuitry consists of TR1, TR4 and IC2. The transmit trigger is coupled to the STC gate generator by C8. This circuit uses only the negative portion of the transmitter trigger to generate the STC pulse. The positive portion is removed by CD1. TR1 will be turned "ON" and C21 will charge. When the trigger pulse ends, TR1 will be turned "Off". C21 can then discharge back to 0V through R13 and RV1. The rate of C21's discharge will be determined by the time constants of R13, RV1, C21.

The STC signal resulting from C21 discharging is combined with the Gain control voltage and Range rate voltage from IC4 and TR7 at TR3 and applied to the IC1 and IC2(CAE-333).

2.4.6 Main Bang Suppression (MBS) Circuit(CCG-161)

The main bang suppression circuit is intended to blank the nearby transmitter energy at the start of the display sweep. TR2 is used to generate the main bang suppression pulse. This circuit also uses only the negative portion of the transmitter trigger to make the MBS pulse. The positive portion is removed by CD2. TR2 will be turned "ON" with the receipt of the MBS pulse and C13 will charge. When the trigger pulse ends, TR2 will be turned "OFF". C13 will discharge to 0 V through R21. The discharge rate will be determined by the time constant of C13 and R21. The MBS signal is combined with the Gain control voltage, STC signal and range rate signal by CD3-1,CD3-2,CD4 and CD12-1, and applied to the IC1 and IC2(CAE-333) through TR3 emitter follower..

2.4.7 POWER SUPPLY

The power supply converts the ship's mains to necessary DC voltages to operate the radar system. These output voltages include regulated +17VDC, +6VDC, and +250VDC.

The AVR converter consists of IC1 and TR1. The RV1 is normally set by monitoring the +6VDC output at TP1 and adjusting for +6VDC$\pm$O.1VDC with a volt ohm meter.

2.5 DISPLAY UNIT

The display unit normally contains the Main Control PCB, the Power Supply PCB, the CRT and the CRT Display Control PCB, and the Control Panel PCBs.

2.5.1 SIMPLIFIED BLOCK DIAGRAM

FIG. 2-5 shows the fundamental circuits of the display unit in a simplified functional block diagram. Most system operations within the display unit occur primarily on the Main Control PCB (CMC-1008). It is on this PCB that most of the signal processing takes placed. The following is a brief description of the main circuit functions of the display unit.

2.5.2 MAIN CONTROL PCB

2.5.2.1 VIDEO INPUT CIRCUITRY

The incoming video signals from the receiver in the scanner are first routed through the FTC circuit components consisting of CD11 and C52.

The Varicap diode CD11 controlled by the voltage supplied from IC30-(8) which is determined by the front panel RAIN CLUTTER Control.

Maximum FTC occurs when the voltage level at CD11 cathode is 3VDC.

2.5.2.2 A/D CONVERTER

The A/D converter changes the incoming video signal form analog video signals into 3 bit digital signals. The A/D converter consists of IC32-IC36. Since the conversion must occur at high speed, four comparator ICs are used. The MSB threshold level (brightest video) is set by RV2(Upper) located in the Main Circuit PCB. The digitized video output (RVDP0-3) is then sent to IC12, the Main LSI and Video Processor for storage into the video buffer memory.

2.5.2.3 MAIN LSI/VIDEO PROCESSOR

The Main LSI, IC12 receives the bearing pulse signals(BP) generated by the antenna motor/encoder assembly to synchronize the timing of the scan converter and to control the various clock inputs and outputs for the video memory and display.

When the bearing pulses are received, IC12 also generates the radars transmit trigger at TIYLOU. The trigger signal is sent over to IC27 where the ONM delay timing adjustment is applied. The output at IC37-12 and IC37-13 provide complementary drive signals to TR5 and TRD6. These amplifiers boost the output transmit trigger(TI) level to +12 VDC in amplitude.

2.5.2.4 SAMPLE CLOCK GENERATOR

The sampling clock oscillators generate the frequencies necessary to create the various timing signals including those used for controlling the processing of the digital video signals into the memories.

The Sampling Clock Generators consist of crystals CX2 and CX3, along with IC13 and IC10. The CX2 operates at 30MHz and CX3 operates at 22. 3MHz. The 22.3 MHz frequency is used to determine the timing clocks used on the 0.125, 0.25, and 0.5 and 24 nm range scales, while the 30 MHz clock frequency is used to determine the

clock timing on the remaining range scales.

The Video Processor is contained in IC12 (Main LSI) and can perform two additional processing functions on the video signal when activated in menu. They are: Interference Rejection Processing and Target Expander Processing.

The Interference Rejection Processing is performed by comparing the bit-by-bit content of the digital video stored from each successive radar transmission whenever the IR function is enabled by the operator. The Expander Processing is performed by extending the target digital video pulse length up to 8 additional digital video cells whenever target expansion is enabled in the menu.

2.5.2.5 BUFFER MEMORY

IC14 and IC15 are Buffer Memories capable of 2K word×8 bit dual port input data and output data handling. The buffer memories are used to temporarily store the digitized video input signals according to the clock timing for the range scales in use for the video processor. Memory IC15 is used to store the IR processed video data memory.

2.5.2.6 VIDEO MEMORY

The start of the data readout of the video buffer memory is triggered on the trailing edge of the bearing pulse from the scanner unit. This clock is used for data processing in IC12.
The processed video data which has passed through IC12 is now transferred to the video memory IC31. IC31 is a DRAM consisting of 256K×4 Memory planes which are used to produce the picture data, EBLs, VRM, video (including wakes) range rings, etc.

The address signals used to write into the read out of the video memory are generated in IC12. The output data from the video memory is entered into IC20, the video signal mixer/processor.

2.5.2.7 CONTROL PCBS

The RADAR 2000 use a keypad control PCB assembly(CCK-781) to activate the radar system and control its functions. These keypad control PCBs interface directly into the Main Control PCB via connectors J6 and J7 to IC5(CPU) on the Main PCB.

CCK-781 contains 13 key switches and the jog dial controls for the TUNE, GAIN, FTC, STC and RANGE. Each panel includes LEDs for backlighting the keyboard panels and the LED intensity can be controlled in 8 levels of brightness by the menu selection via the DIM line. The operating voltage for the LEDs originates at IC29 and TR3 on the Main PCB.

2.5.2.8 GDC(GRAPHIC CONTROL MEMORY)

This radar uses an 8 bit CPU(IC5), and a Graphic Display Controller(IC7) to principally control the graphic system of the on-screen display of VRM, EBL, Bearing Scale, Range markers, and other graphic characters. The CPU receives operating instructions from the 1 Mbit EPROM in IC3 and system setting stores data in the 64

Kbits of RAM available in IC2.
The RAM memory has a battery backup through IC1 so that the settings of Range, EBL, VRM, CRT brilliance, EXP, and IR will be maintained in memory after each shutdown of power.

The DGC(Display Graphic Controller) paints the various character data, VRM, EBL, Range Marker, etc. under direction of the CPU to the graphic DRAM memories IC23-IC27 while performing processing of the data from bearing pulses, reset pulses and from the keys on the control panel.

The contents of the DRAM memories is read out by input parallel-serial converters located in IC12 and ultimately is sent to IC20.

2.5.2.9 VIDEO OUTPUT

In IC20, data which has been written into the Video Mixer/Processor by the range and sample clock timing signals will now be read out to the CRT monitor in raster scan timing; that is, the Horizontal frequency of 15.750 KHz and 60 Hz Vertical frequency.

The 3 bit digital video signals are re-converted by IC20, into analog video signals having 8 levels and outputted to the buffer amplifier TR2. The graphic data is input to TR2 via CD1 and IC20. When the brilliance control is changed in the function menu, the brilliance control signal is outputted from IC28 and applied to TR1. The CRT brilliance will be varied in 8 steps. The combined video signals(radar targets and display graphics) along with the horizontal(HS) and vertical(VS) synchronization signals are sent to the monitor display.

2.5.2.10 DISPLAY MONITOR

The display monitor receives its operating supply voltage from the +12VDC supplied by the power supply PCB. The video signal is sent to TR4 and TR5 amplifiers before coupling to the CRT cathode. RV1 sets the contrast level of the video for the CRT.

The horizontal sync signal operates the horizontal oscillator IC1. The oscillator provides the drive via TR6 to run the HV flyback transformer and generate the operating voltages for the CRT as well as the horizontal deflection coil.

The vertical sync signal operates the Vertical oscillator IC3 via IC2. The oscillator output at VOUT drives the vertical deflection coil.
Traditional adjustments are provided to set the focus, CRT brightness, vertical hold, size, and linearity, horizontal hold, and the video contrast.

The CRT is mounted and arranged in the "portrait" mode in the radar. Therefore, the horizontal adjustments will effect the vertical picture and vice-versa, the vertical adjustments will effect the horizontal aspects of the picture.

2.5.3 OPTIONAL INPUTS

The RADAR 2000 can receive various input signals from Navaids and Flux Sensors.

If more than one data type is present at the radar inputs(for examples; flux sensor and NMEA) a system priority has been established in the radar's software to respond to the inputs in driving the features. The assigned priorities are set in this manner:

HEADING: 1. Flux Sensor(NMEA 0183"HDM, HDT, HSC" sentences)
2. Sea Talk Data (Heading via Autopilot compass)
3. Navaid Data (NMEA 0183 "RMC, RMA, VTG" sentences)

POSITION: 1. Navaid Data(NMEA 0183 "RMC, RMA, GLL, GTD" sentences)
2. Sea Talk Data

SPEED: 1. Navaid Data(NMEA 0183 "RMC, RMA, VTG, VHW" sentences)
2. Sea Talk Data

WAYPOINT: 1. Navaid Data(NMEA 0183 "RMB, BWC" sentences)
2. Sea Talk Data

The NAVAID input is connected at J403, Pins 1 and 2. The signal is coupled via J9-1,2 to section 1 of IC40 (Optical Isolator), to Inverters 1 and 2 of IC39, to the data selector IC38, to UART IC6, and finally to the CPU IC5.

The HEADING data input essentially follows a similar route. The signal is connected at J403 Pins 3 and 4, coupled via J9-3,4 to section 2 of IC40(Optical Isolator), Inverters 3 and 4 of IC39, to the data selector IC38, to UART IC6, and finally to the CPU IC5.

2.5.4 POWER SUPPLY (CBD-1283)

The Power Supply converts the ship's DC input voltage to the necessary DC voltages to operate the radar system. These output voltages include regulated +12 VDC, -12 VDC, +5VDC, -5VDC, and +24VDC.

The power supply can begin operation when the STBY/OFF switch is pressed on the Control (Right) PCB. The STBY signal toggles IC5-2output and TR8 conducts. This enables the Vcc supply to the AVR converter circuit. IC4 is the 90 second timer and its output at pin 3 via IC2/2 becomes the OPE signal to put the radar into the Transmit mode. When the XMIT/OFF key is pressed, IC5-12 operatesTR7 and IC2/2 to enable the OPE output.

The AVR converter consists of IC1, IC2, and IC3, as well as TR3, TR4, TR5, and TR6. IC1 controls the switching of the power FETs TR5 and TR6. Sensing of the AVR output occurs from the +12VDC line, sampled via RV1, compared at IC3 and controlled via IC2/1 to the AVR. RV1 is normally set by monitoring the +5 VDC output at TP1 and adjusting for +5VDC, +/-.1 VDC with a volt-ohm meter.

When both the POWER and XMIT/STBY keys are pressed together, IC6resets IC5 outputs and disconnects the Vcc from the AVR IC1.
This will turn the power supply and the radar system to OFF.

SECTION 3

TROUBLESHOOTING

3.1 TROUBLE-SHOOTING GUIDE

While the radar 2000 is highly reliable systems, early signals and detection of component fatigue can sometimes be spotted during regular operational checks.

When a problem is observed, corrective service should be arranged to avoid failure at critical times at sea.

3.2 MASTER RESET

The first step in attempting to clear a problem associated with the general operation of this Radar is to perform a MASTER RESET(There are two methods.).
This can be done by pressing

EBL/VRM key(to remain initial setting data) or

OFFCENT and BRIL/DIM keys(to clear initial setting data)

and while holding, turning the power on. This should be performed anytime a component or PCB within the Radar is replaced. This function will clear the Radar's memory and
will return it to its factory settings. It may then be necessary to make the INITIAL SETTING and to re-enter the parameters previously established by the operator.

It should be noted that micro-components within the Radar are generally not field replaceable, therefore, repairs to the Radar typically go down to the board level only.

> CAUTION
> In making checks, be alert to the high voltage points existing
> Throughout the equipment.

3.3 FUSE

A fuse seldom blows out without some cause. Even if a fuse is merely replaced and does not blow again, it still may be necessary to make further checks of the circuits associated with the fuse.

TABLE 3-1 shows a table of fuses employed in the equipment.

TABLE 3-1 FUSES USED

Location	Part No.	Rating Current	Protective Circuit	Type	Remarks
DISPLAY	F401	8A	All circuit	Glass tube	8A dc 12V
DISPLAY	F401	4A	All circuit	Glass tube	4A dc 24 V, 32V
DISPLAY	F402	5A	Scanner motor	Glass tube	5A dc 12V
DISPLAY	F402	3.15A	Scanner motor	Glass tube	3.15A dc 24V, 32V

3.4 FAULT FINDING PROCEDURE

Often the display on the CRT can help indicate which major circuit is at fault. It may be quicker to check-out the equipment according to the trouble shooting guide that follows(TABLE 3-2).

In general, the common causes of trouble frequently encountered include abnormal resistances, intermittent variable resistors, switches and relays.

In the following fault finding procedure, it is assumed that only a VOM(volt ohm meter) is available; the use of an oscilloscope simplifies the procedures and may prove necessary in some cases.

TABLE 3-3 is the trouble shooting guide and check-out procedure, TABLE 3-4 shows typical voltages and resistances at significant points throughout the equipment. The internal resistance of the tester used in measurements was 20 kΩ/V dc, 8 kΩ/V ac.

TABLE 3-2 OPERATION CHECKLIST

Unit to be checked	Check item	Correct condition	Remarks	Measuring point
Scanner Unit	a. Input voltage	+12V -12V		CME-229-W1-1,2
	b. AVR output voltage	350V	X-MIT	CME-229-CD6-K
	c. Mag. Current	12V～20V		CME-229-TP1 or
Display Unit	a. Input voltage	Refer to Note		J401-1-2
	b. AVR output voltage	5V		TP1-ground
	c. Observation of Screen sensitivity, Sweep length, sweep linearity, sweep center, ring and illumination.			
	d. Check of the operating controls			

NOTE
Allowable variation of input voltage, DC10.2V-42V

TABLE 3-3 TROUBLE SHOOTING GUIDE

	Trouble	Remedy
1.	Does not start at OPERATE switch to STBY.	Check: [DISPLAY] Blown fuse F401. Check input power circuits. Check modulator circuits in scanner. Faults of contact on CCK-733. Faults of power supply circuit on CBD-1283. Faults of contact on connector of CBD-1283. Faults of rectifier diodes on CBD-1283.
2.	Scanner fails to Rotate.	Check: [SCANNER] Fault of S101. (Safety Switch OFF) Fault on contact on terminal boards. Fault of M101/B101. Fault of drive mechanism. Faults of modulator circuit on CME-229
3.	Scanner rotates but Rotation of sweep is Abnormal.	Fault of connection between M101/B101. Check: [DISPLAY, SCANNER] Fault of encoder(BP). Fault of main circuit for the Display Unit.
4.	No picture on the screen.	Fault of CRT display unit or its supply Voltages. Check: [DISPLAY] Open heater of CRT. Fault of contact on CRT socket. Fault of contact on CRT cap. Fault of video circuit.
5.	Only horizontal line screen.	There may be fault in vertical sweep Generator, amplifier circuits and Deflection coil. Check: [DISPLAY] Fault in vertical sweep generator, Amplifier circuit.
6.	Incorrect sweep	Adjust CENTERING MAGNET.

	– Start of sweep is not centered on the screen.	Adjust horizontal or vertical hold. Adjust vertical length and linearity. Adjust height as necessary.
	– Markers are oval.	Adjust horizontal length.

	Trouble	Remedy
7.	Range rings on the screen but no noise and no echoes:	Fault circuit between IF amplifier of Receiver unit and input circuit of display Unit video amplifier. Check: [DISPLAY] Fault of GAIN, STC control settings. Fault of receiver unit. Fault of contact on terminal boards and Connector.
8.	Noise and range the screen but no echoes.	If no transmission is present, check the Modulator and magnetron. Check: [SCANNER] If transmission appears to be present as Indicated by the correct MAG. I reading on Tester. CME-229 TP1=12VDC～20VDC Failure of Local Oscillator tuning. If transmission appears to be present, Carry out the Local Oscillator tuning Procedures and check the MIC. Fault of MIC Mixer. If no transmission is present, Whether the Lead wire to magnetron is grounded to Chassis. Fault of magnetron.
9.	Poor sensitivity. Dim echoes.	Check: [SCANNER, DISPLAY] Reduction of transmitting output power. Fault of magnetron. → Check of MAG. I reading on PC101-TP1. Fault of MIC Frontend. Fault of CRT. Failure of Local Oscillator tuning. Failure of FOCUS adjustment. Failure of INTENSITY ADJ. Fault of video amplifier circuit on CMC-1008 (Main Circuit) Fault of receiver unit.
10.	NO VRM or VRM cannot be controlled.	Check: [DISPLAY] Fault of CCK-781. Fault of main circuit(CMC-1008).
13.	NO EBL or EBL cannot be controlled	Check: [DISPLAY] Fault of CCK-781. Fault of main circuit(CMC-1008).
14.	No alarm zone marker,	Check: [DISPLAY]

8		∞ ×10		GND
9		0 ×10		2048 BP
10		7 ×10		RRATE
		300		

DISPLAY UNIT(Without Interunit Cable connected)

Measuring Point	Resistance (∞)
J402 1	∞ ×10
2	∞ ×10
3	23 ×10
4	0 ×10
5	0 ×10
6	6 ×10
7	54 ×10
8	0 ×10
9	∞ ×10
10	21 ×10
11	4 ×10
12	1K ×10
13	50 ×10
14	3.5 ×10
15	42 ×10
16	24 ×10

SECTION 4

MAINTENANCE

4.1 GENERAL

It is necessary to perform the maintenance services listed below to keep the RADAR 2000 in good working conditions. Proper maintenance of the RADAR 2000 minimizes the possibility of machine failures. The maintenance operations that are common to all components of the RADAR 2000 is listed below.

(1) Cleaning

Remove dirt, dust, or water-spray from the RADAR 2000 enclosure and keep it as clean as possible. Use a dry lint-free cloth.

(2) Screw inspection

Check the screws used to assemble and secure the components of the RADAR 2000 for loose connection.

(3) Cabling check

Check the cables connecting between the components (between the scanner unit and display unit, display unit and power supply, and display unit and optional devices) for poor connection.

Caution: When servicing the RADAR 2000, be sure to turn it off to prevent electric shock. If a rectifier unit is used, in particular, turn off power to the display unit. Note that voltages from the rectifier unit are always present even if the radar is stopped.

4.2 SCANNER UNIT

When inspecting the scanner unit of the RADAR 2000, be sure to turn off power to the display unit. Keep a watch or magnetic cards away from the modulator block as it contains a magnetron having a strong magnetic force.

4.2.1 Radome Scanner Unit

(1) Radome

A radome surface contaminated by smoke, dust, or paint would cause attenuation or reflections of radio waves, resulting in reduced radar performance. Periodically check the radome scanner unit. If it proves dirty, wipe the radome surface with a soft lint-free cloth moistened with alcohol or damped cloth.

* Never use solvents such as thinner, gasoline, benzene, trichlene, and ketone.

(2) Lubricating gears

(A) Apply grease to gears evenly using a knife or brush. This lubrication needs to be performed at least semiannually. The shorter the lubrication period, the longer the gears will endure.

Use Mobilux No. 2 from Mobile Oil Co., Ltd. or equivalent.

(B) Check the mounting bolts for loose connection occasionally.

4.3 Display Unit

4.3.1 Cleaning the Display Unit Screen

Dust on the CRT tube would reduce the glass transparency and make the video image dim. Wipe the screen surface with a soft lint-free cloth (made of flannel or cotton). A cloth moistened with an antiseptic agent would cause little problem. When using it, wipe softly; never rub the screen surface with force.

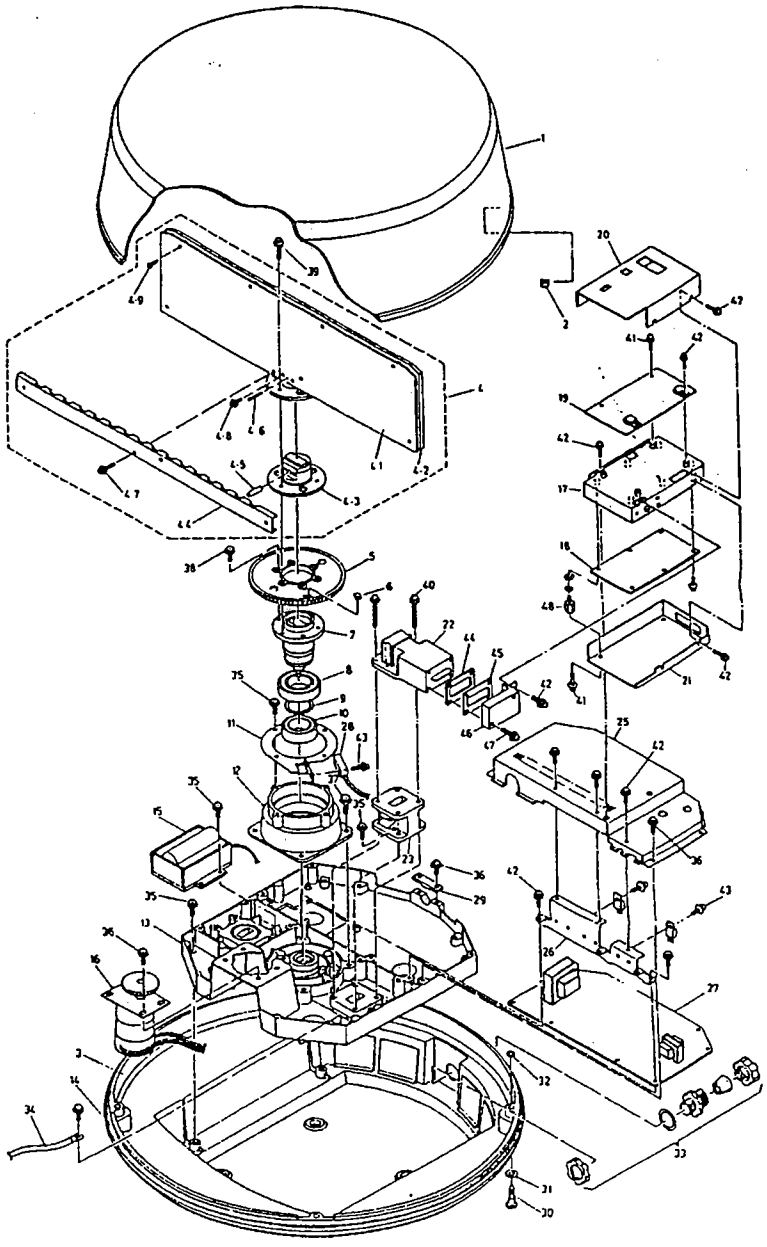
APPENDIX

RADAR 2000 PARTS LIST

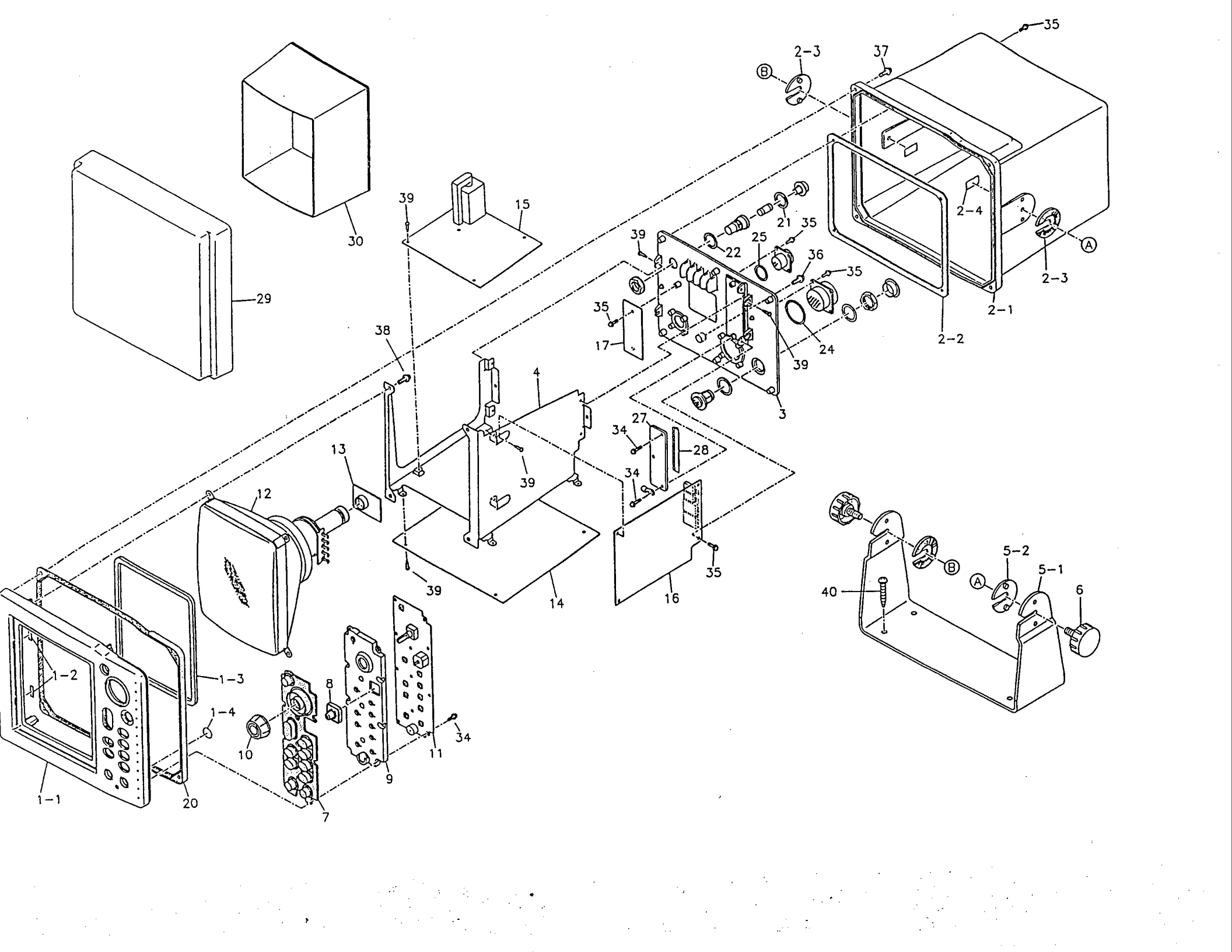
Description	Part No.	Drawing (FIG)
Radome Scanner Unit	NKE-1044A	106
Receiver PCB	CAE-333	108
MIC (E301, NJT1946A)	5EZAA00029	106
Diode Limiter (A101, NJS6931J)	5EZAA00033	106
Modulator PCB	CME-274	107
Motor Assy (M101)	7BDRD0023	106
Magnetron (V201, M1528)	5VMAA00075	106
SHM Switch	5KRAA00058	106
GS control	CCG-161	109
Display Unit	NCD-3744	110
Power Supply PCB	CBD-1283-J	114
Main Control PCB	CMC-1008	111, 112
Control PCB	CCK-781	113
CRT Assembly		114
Consisting of:		
CRT Monitor PCB	CCN-245	115
CRT	7WZRD0025	

機械部品諸元表
NKE-1044A

番号	名称	JRCコード	
1	レドーム	MPBX21381	①と②で MPBX21382
2	ボス	MTL033810A	
3	外きょう	~~MPBX17822A~~	→ MPBX36497
4	輻射部	MPAE00501	↗ MPBX17822D
4-1	アンテナプリント板	-	
4-2	アンテナサポート	MTV002334	
4-3	トランスデューサ	MPAB01685	
4-4	シールド板	MTB144766	
4-5	絶縁体	MTT020321	
4-6	内導体	MTL033817	
5	平歯車	~~MTV002340~~	⑤と⑥で MPGK02946
6	マグネット	-	
7	ロータリージョイント	MPAB01684	
8	ベアリング	BRGK01325	
9	軸用C型止め輪	BRTG01192	
10	ベアリング	BRGK01324	
11	押え板	MTB144765	
12	ハウジング	MTC002285	
13	メインシャーシ	MPBC07978	
14	-	-	
15	マグネトロン	-	
16	エンコーダ付DCモータ	-	
17	受信部	MDEW00910	
18	ダイオードリミッタ	-	
19	シールドカバー	MPSC00703	
20	変調部	MDMW10506	
21	リードスイッチ	-	
22	クランプ	MTC003327	
23	-	-	
24	ロープ	MPXP01279	



ASSEMBLY DRAWING, SCANNER UNIT(NKE-1044A)


35
2-3
37
B
30
39
15
29
39
21
35
22
25
36
35
2-4
A
2-3
2-1
2-2
35
17
38
4
24
39
3
27
34
28
13
12
39
34
39
14
16
35
5-2
5-1
40
B
A
6
1-2
1-3
1-4
8
10
9
11
34
1-1
20
7

ASSEMBLY DRAWING of NCD-3744

Location	Description	JRC Code	Symbol
27	PLATE, RETAINING	MTB186295	
28	SPRING	MPSR30054	
29	SUN COVER	MTV301855	
30	SUN SHIELD	MTV301854	
31	BLANK		
32	BLANK		
33	BLANK		
34	SCREW with captive washer	BSNC03010B	
35	SCREW with captive washer	BSNC03008B	
36	SCREW with captive washer	BSNC04008B	
37	SCREW with captive washer	BSNC04014B	
38	SCREW with captive washer	BSNC04012B	
39	TAPPING SCREW	BRTG03095	
40	TAPPING SCREW	BRTG03217	

ASSEMBLY DRAWING OF NCD-3744

Location	Description	JRC Code	Symbol
1	BEZEL ASSY	MPBC32831	
1-1	FRONT BEZEL	MTC300598	
1-2	RUBBER SEAL	MTT300800	
1-3	CRT GASKET	MTT300871	
1-4	POROUS FILM	BRPK05033	
2	CABINET ASSY	MPBX34751	
2-1	CABINET	MTC300599	
2-2	PACKING	MTT304229	
2-3	WASHER, SERRATION	MTV002834	
2-4	RUBBER PLATE	MTT304265	
3	HEAT SINK	MTC300600A	
4	CHASSIS	MPBC32833	
5	BRACKET ASSY	MPBX34752	
5-1	BRACKET	MTB331859	
5-2	WASHER, SERRATION	MTV002834	
6	KNOB	MPTG30053	
7	RUBBER KEY	MTV301852	
8	CAP	MPPK30390	
9	LIGHT GUIDE	MTV301853	
10	DIAL	MPHD30171	
11	CONTROL PCB ASSY	CCK-781	PC3
12	CRT		V202
13	VIDEO PCB ASSY		
14	MAIN CONTROL PCB ASSY	CMC-1008	PC1
15	CRT MONITOR PCB ASSY	CCN-245	
16	POWER SUPPLY PCB ASSY	CBD-1283-J	PC5
17	NOISE FILTER PCB ASSY	CFR-160	
18	BLANK		
19	BLANK		
20	PANEL GASKET	MPPK30387A	
21	PACKING, RUBBER	MTT301170	
22	PACKING, RUBBER	MTT301033	
23	BLANK		
24	O-RING	BRPK05021	
25	O-RING	BRPK05020	
26	BLANK		

LIST OF SCEMATIC DRAWINGS

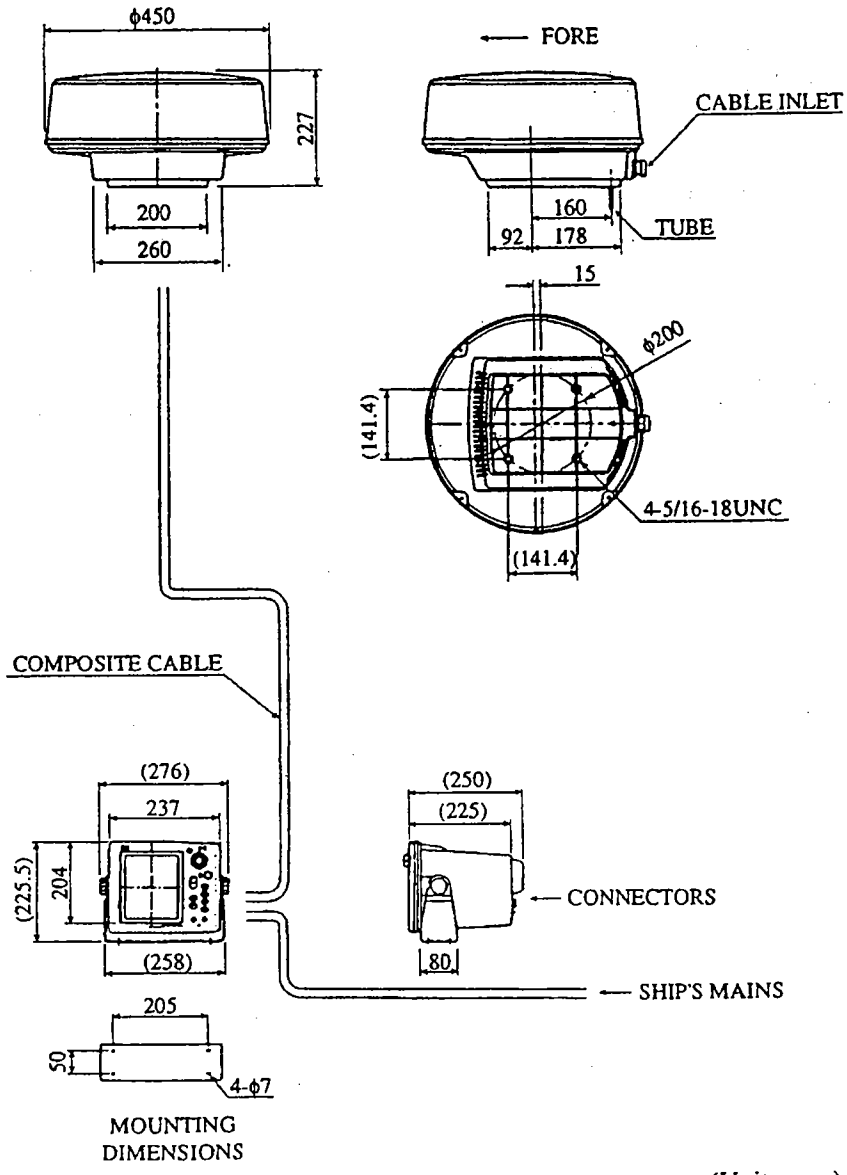


FIG. 101 GENERAL SYSTEM DIAGRAM

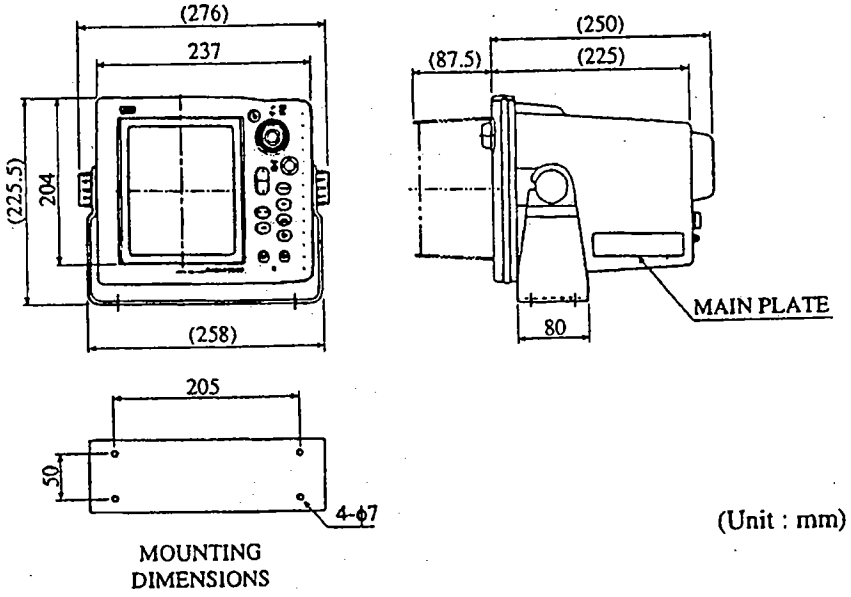


FIG. 102 DISPLAY MOUNTING DIMENSION

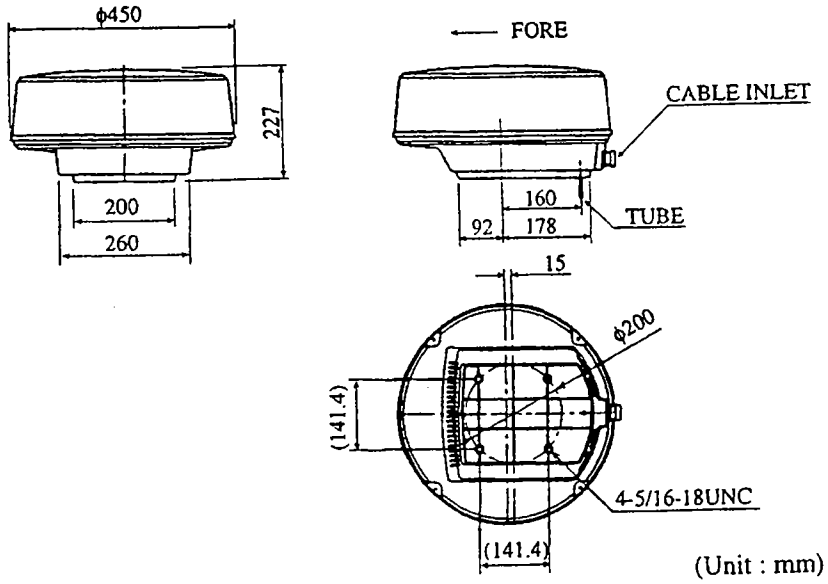


FIG. 103 SCANNER MOUNTING DIMENSION

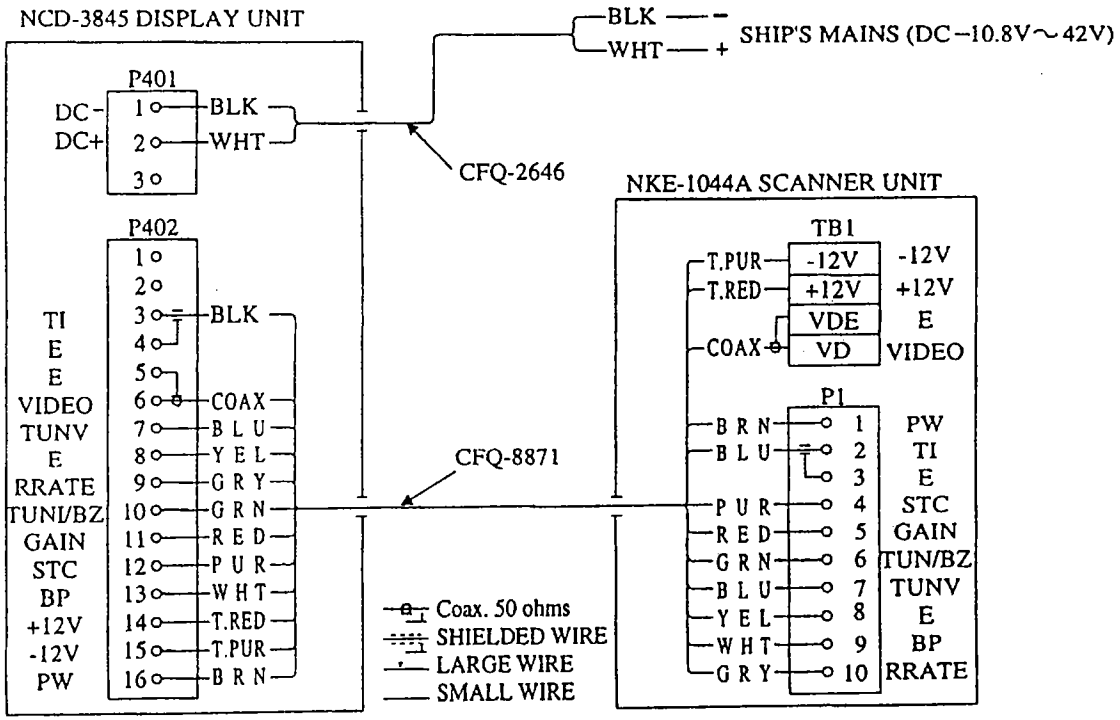


FIG. 104 INTERCONNECTION DIAGRAM

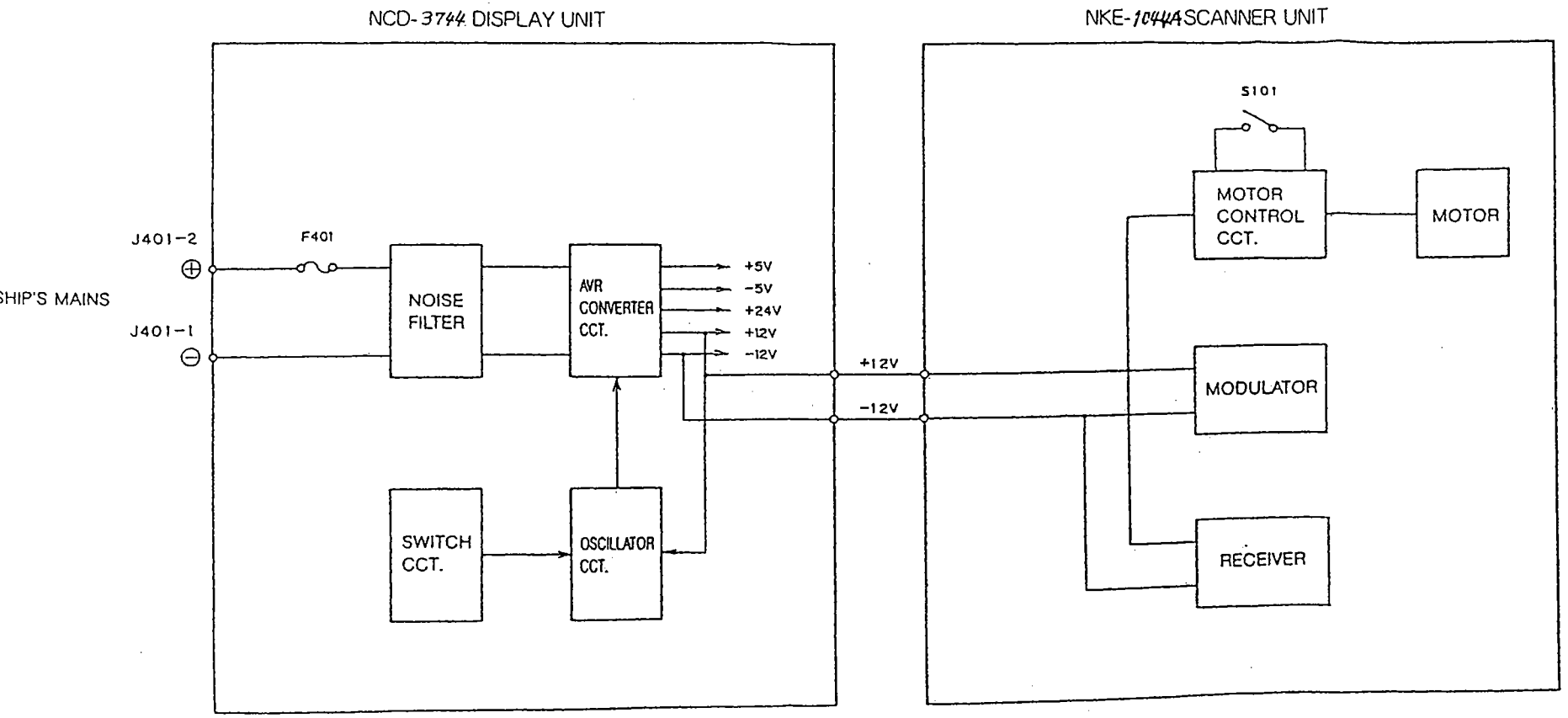


Fig. 105 POWER SUPPLY DIAGRAM OF RADAR 2

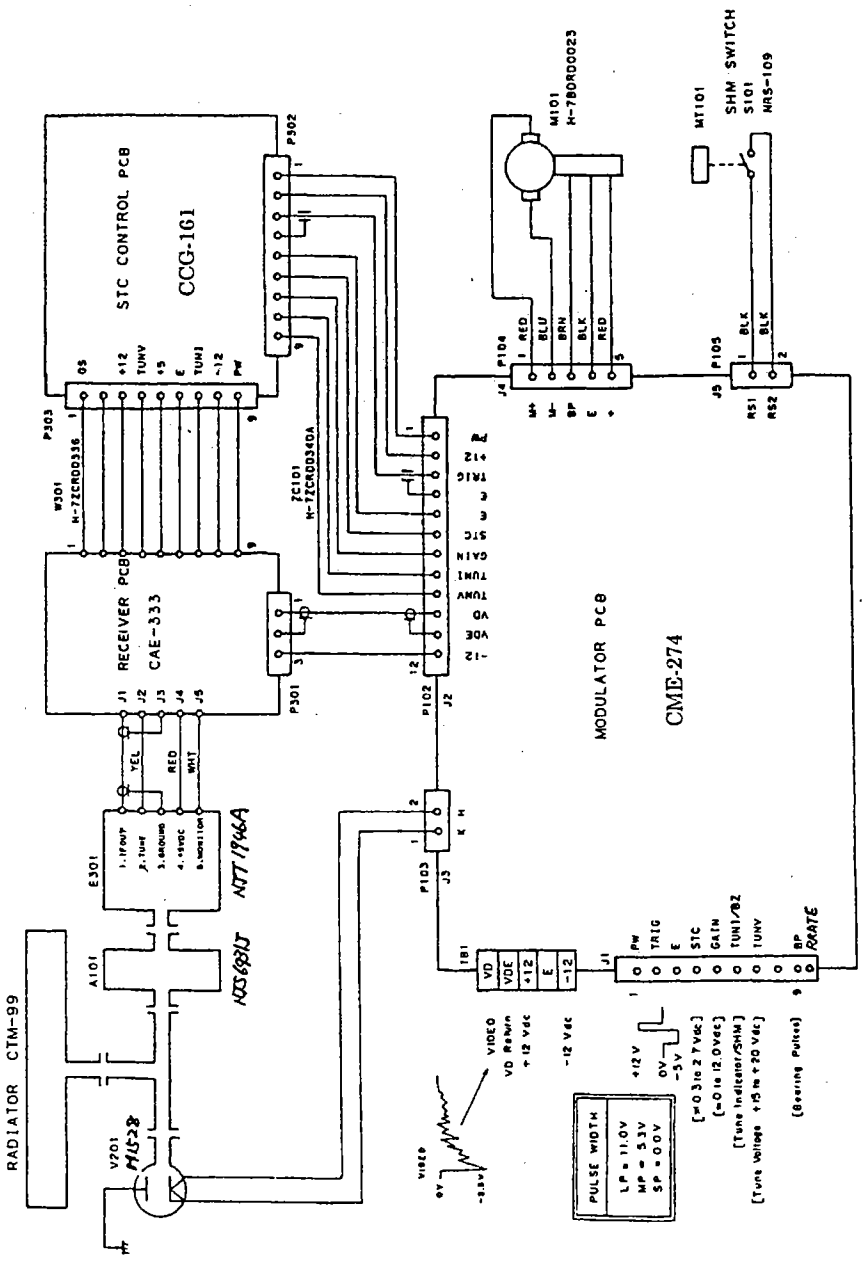


FIG. 106 INTERNAL CONNECTION DIAGRAM, SCANNER NKE-1044A

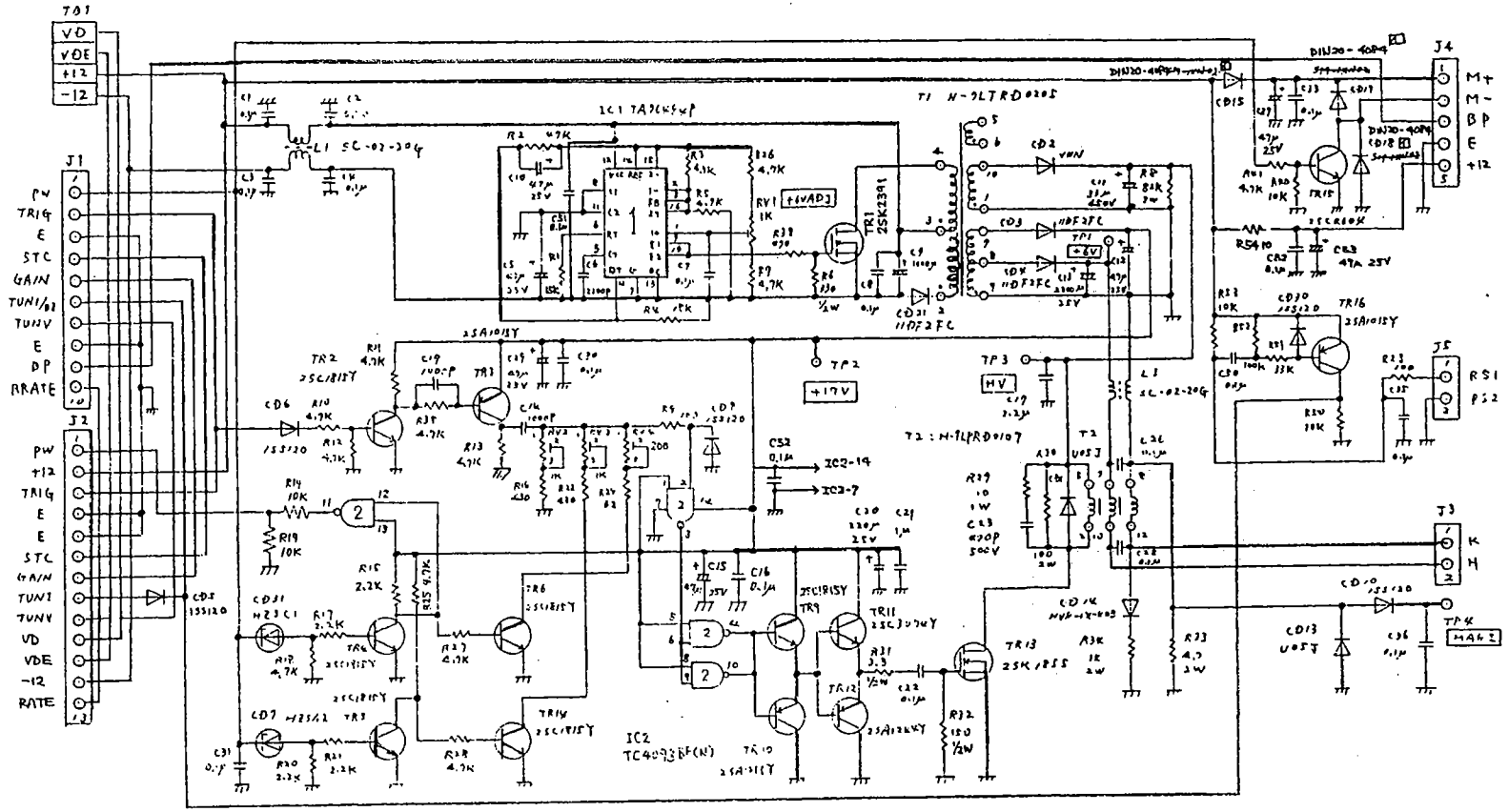


FIG. 107 CIRCUIT DRAWING OF MODULATOR PCB CME-274

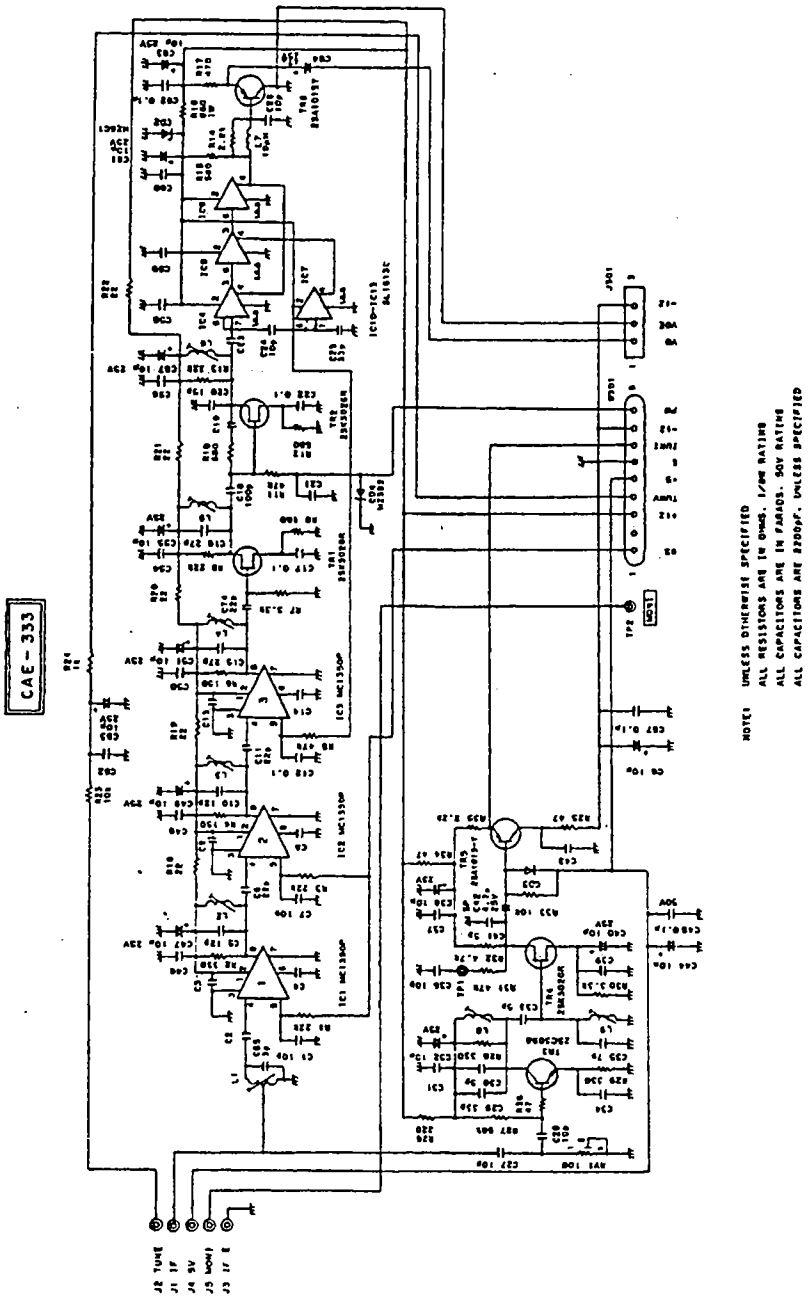


FIG.108 SCHEMATIC DIAGRAM, RECEIVER PCB(CAE-333)

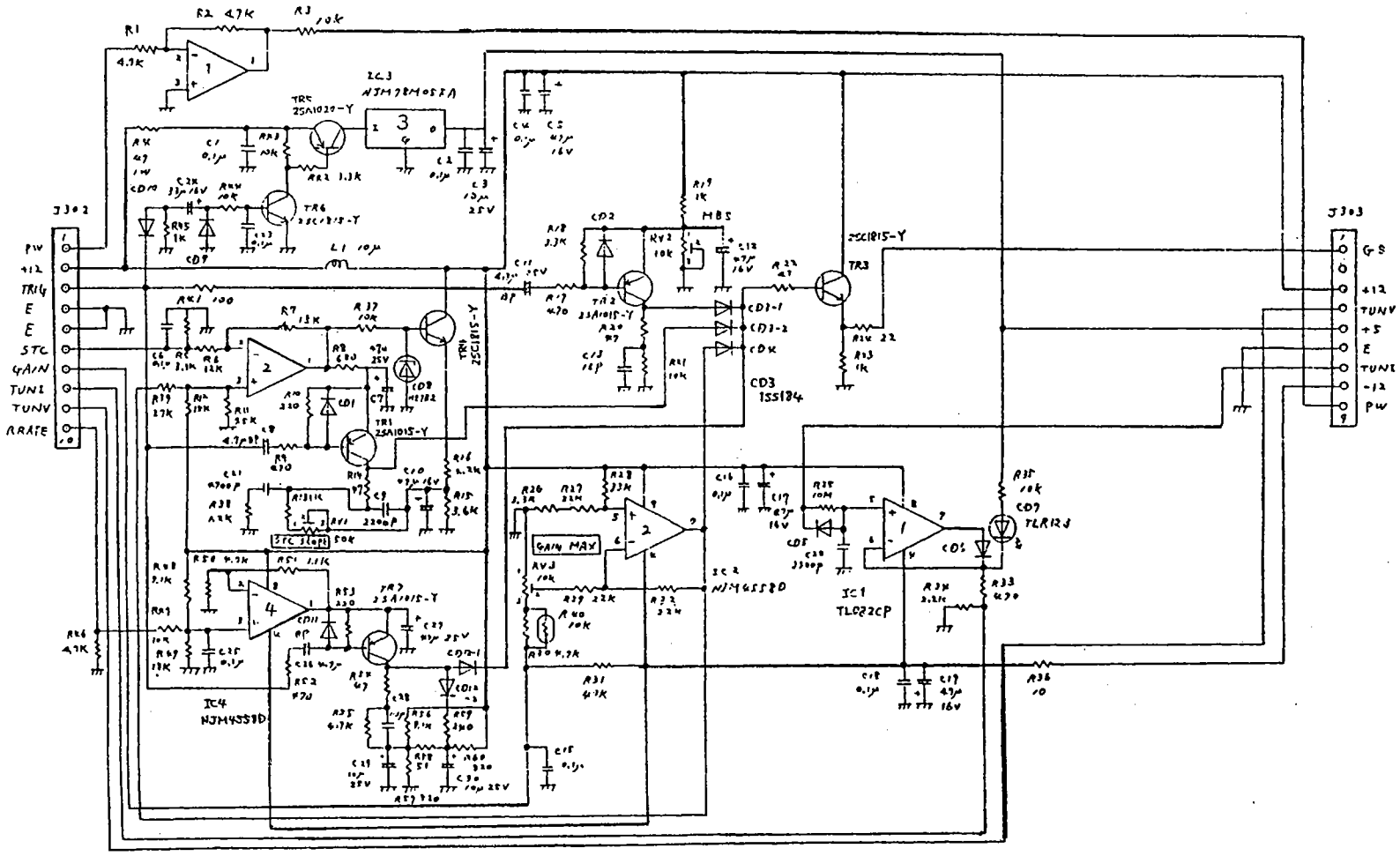


FIG. 109 CIRCUIT DRAWING OF GS CONTROL PCB, CCG-161

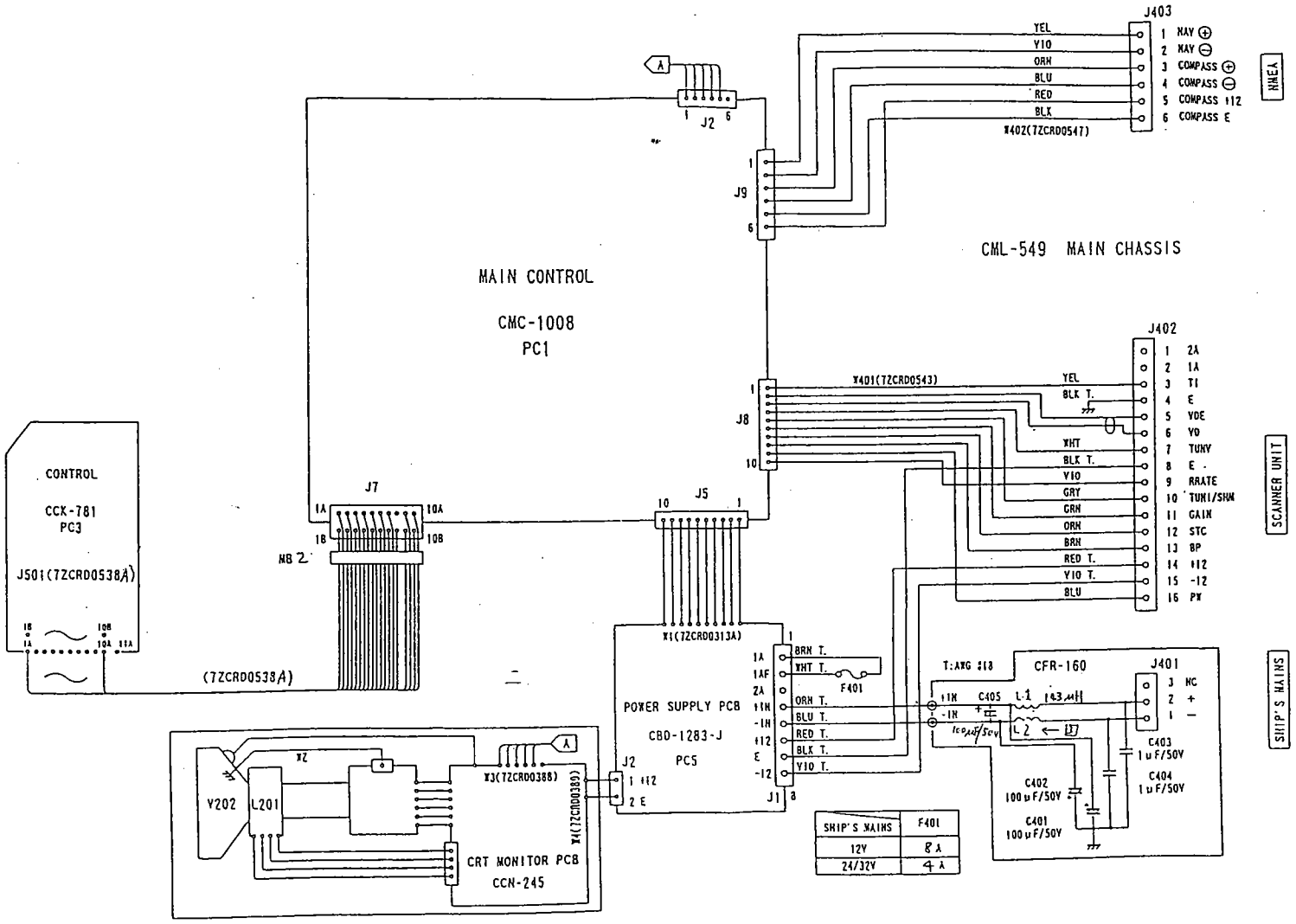


Fig.110 NCD-3744, INTERNAL CONNECTION

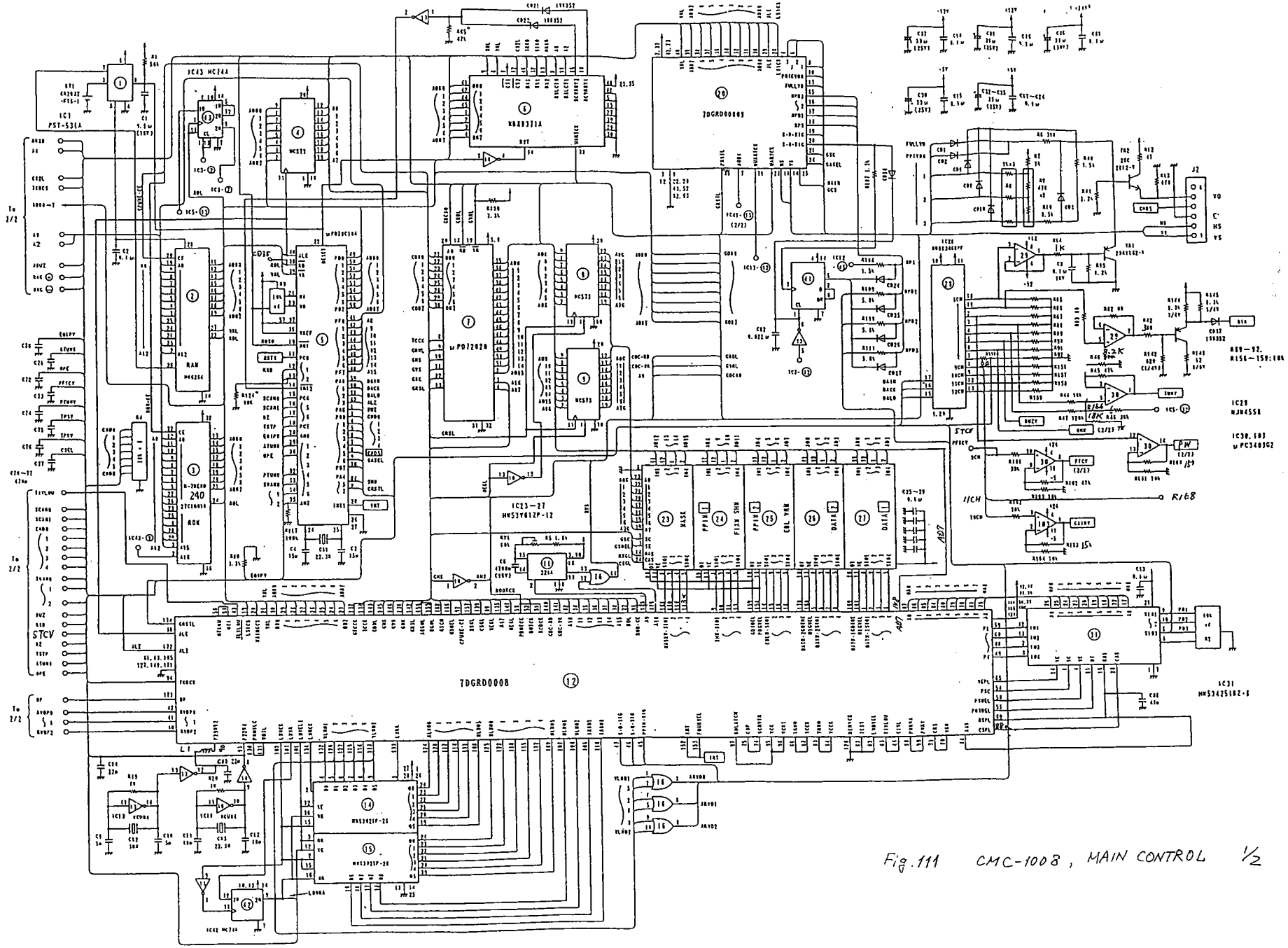


Fig. 111 CMC-1008, MAIN CONTROL 1/2

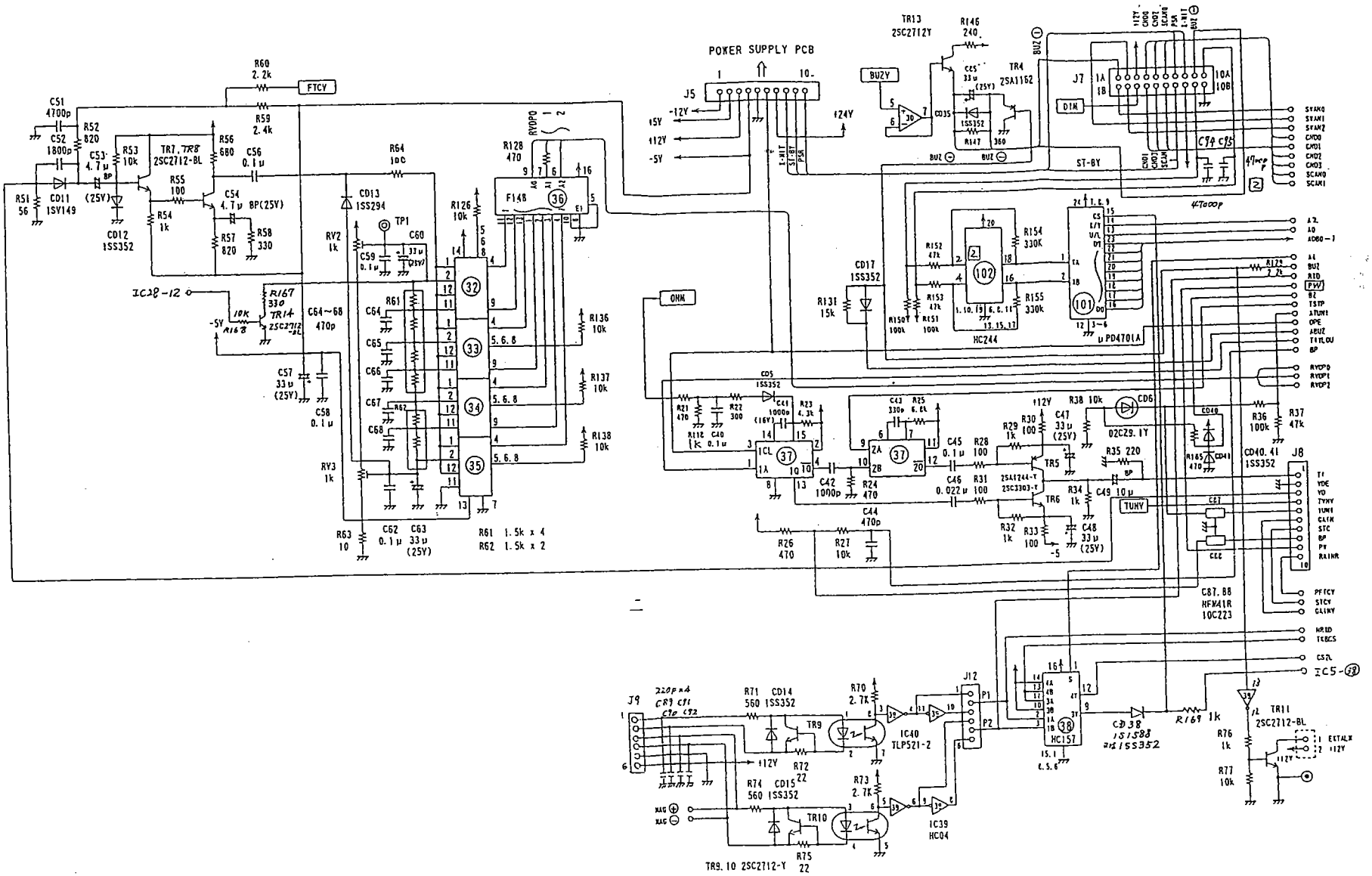


Fig. 112 CMC-1008, MAIN CONTROL 3/3

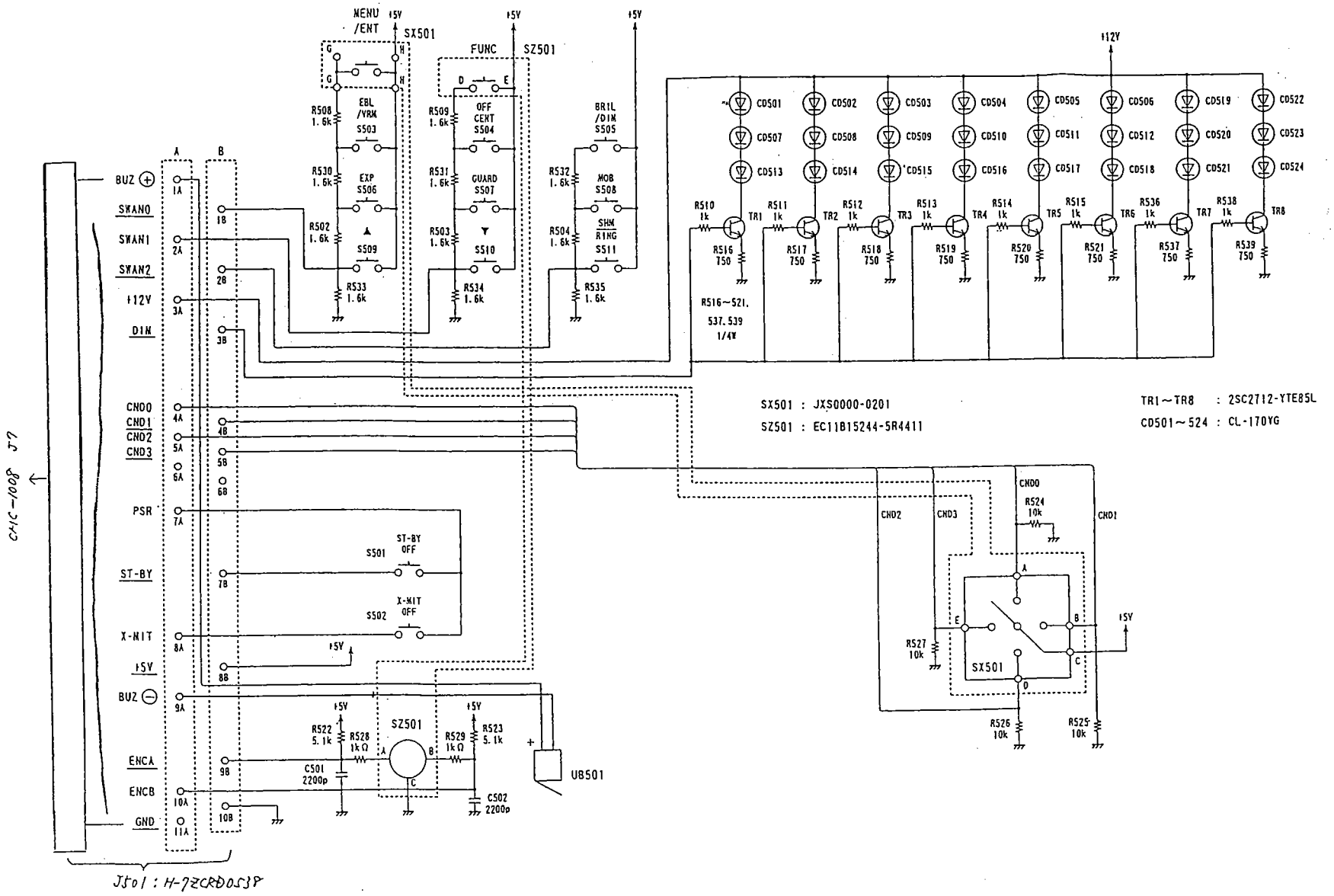


Fig. 113 CCK-781, CONTROL UNIT

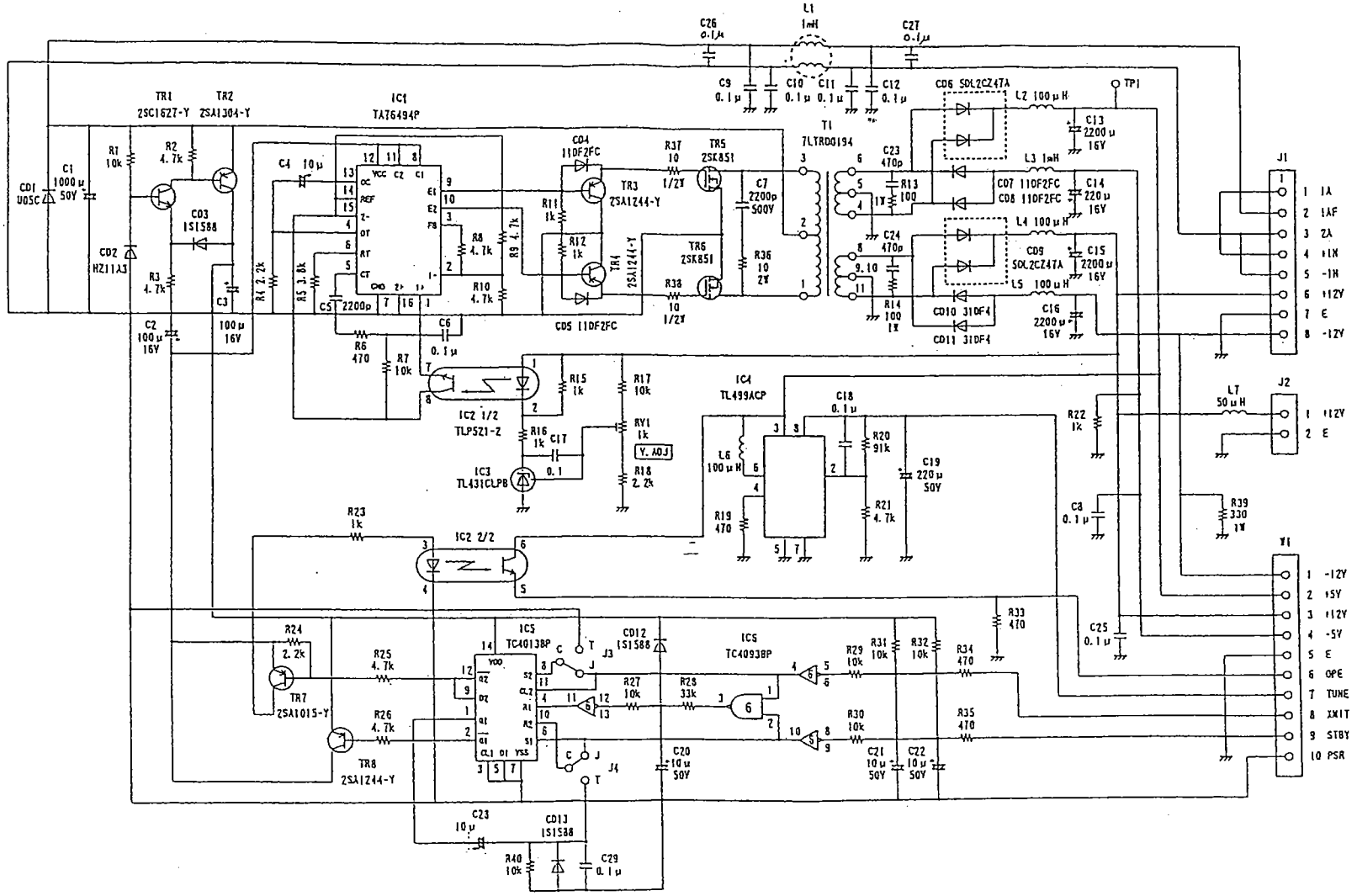


Fig. 114 CIRCUIT DRAWING OF POWER SUPPLY UNIT CBD-1283-J

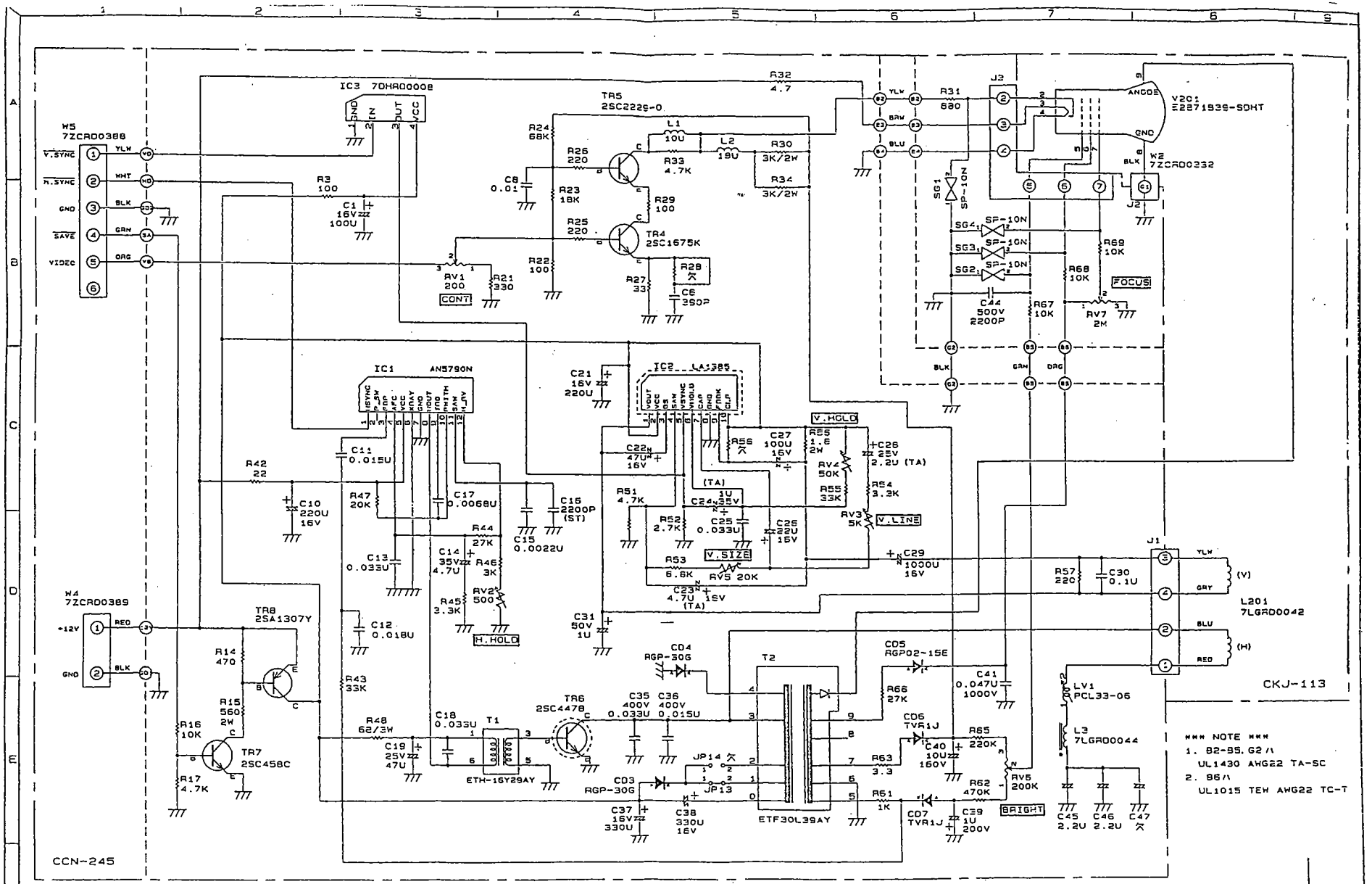


Fig.115

設計 野	製図 波	検図 高	尺度 A 3	名称 7" CRT MONITOR. CCN-245	出図番号 1/1
93.7.06	93.7.06	93.7.06			
JRC 日本無線株式會社				図番 EDOO-7WZRD0025	